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(71) Applicant (for all designated States except US): **INFIN-
NEON TECHNOLOGIES AG** [DE/DE]; St.-Martin-Str.
53, 81669 München (DE).

(72) Inventor; and

(75) Inventor/Applicant (for US only): **NG, Shirley** [SG/SG];
65 Jalan Sukachita, 358940 Singapur (SG).

(74) Agent: **SCHÄFER, Horst**; c/o Kanzlei Schweiger & Part-
ner, Karl-Theodor-Str. 69, 80803 München (DE).

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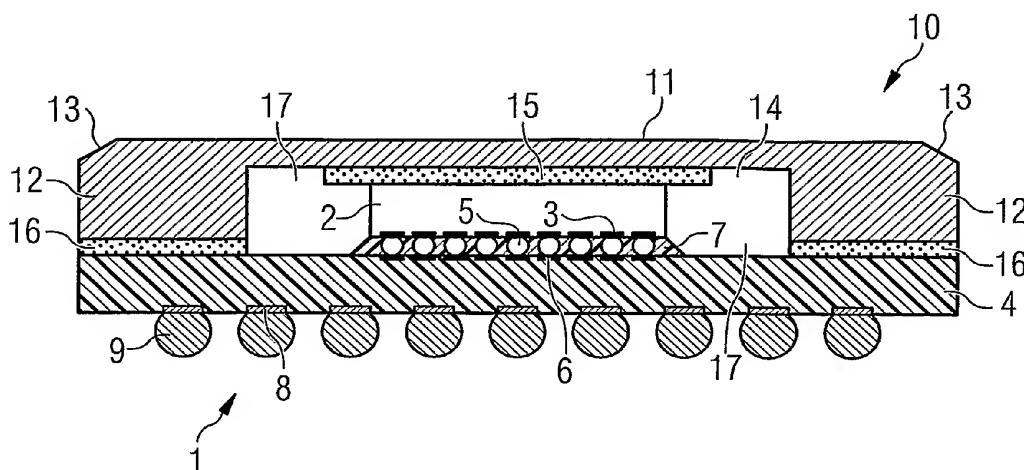
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(54) Title: MATRIX-TYPE SEMICONDUCTOR PACKAGE WITH HEAT SPREADER



(57) Abstract: 1. A semiconductor package (1) comprises a semiconductor chip (2) including an active surface with a plurality of chip contact areas (3) and a package substrate (4) including a plurality of first contact areas (6) and a plurality of second contact areas (8) on its bottom surface. The chip (2) is mounted on the package substrate (4) with its active surface facing the package substrate (4). A plurality of conducting means (5) provide electrical contact between the chip contact areas (3) and the first contact areas (6). A heat spreading means (10) comprises a planar area (11) and at least one protrusion (12). The planar area (11) is attached to the upper surface of the chip (2) and the protrusion (12) is attached to the upper surface of the package substrate (4).

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